



FIG 1

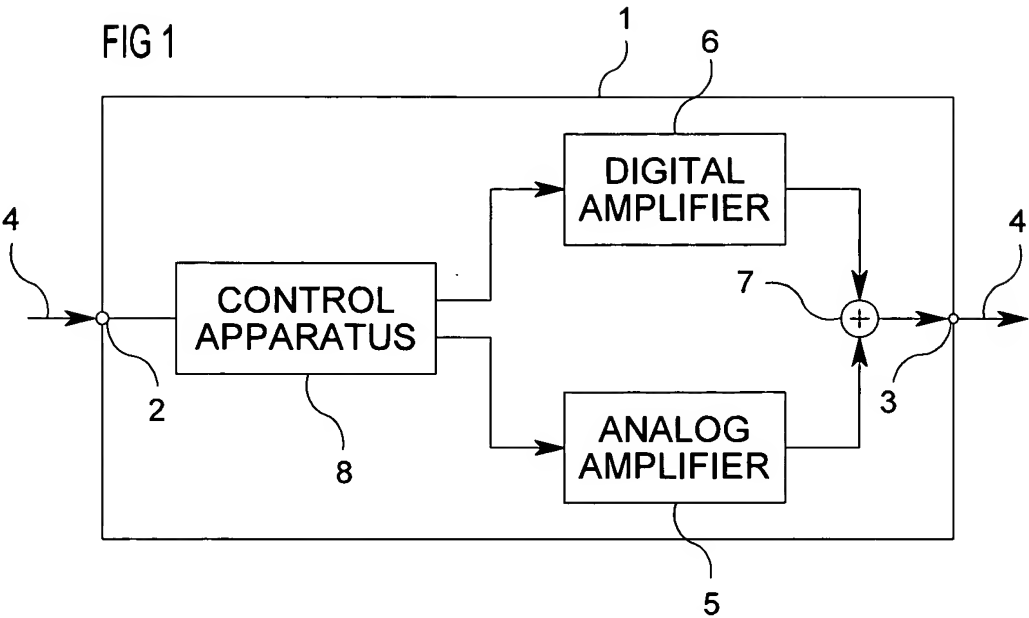
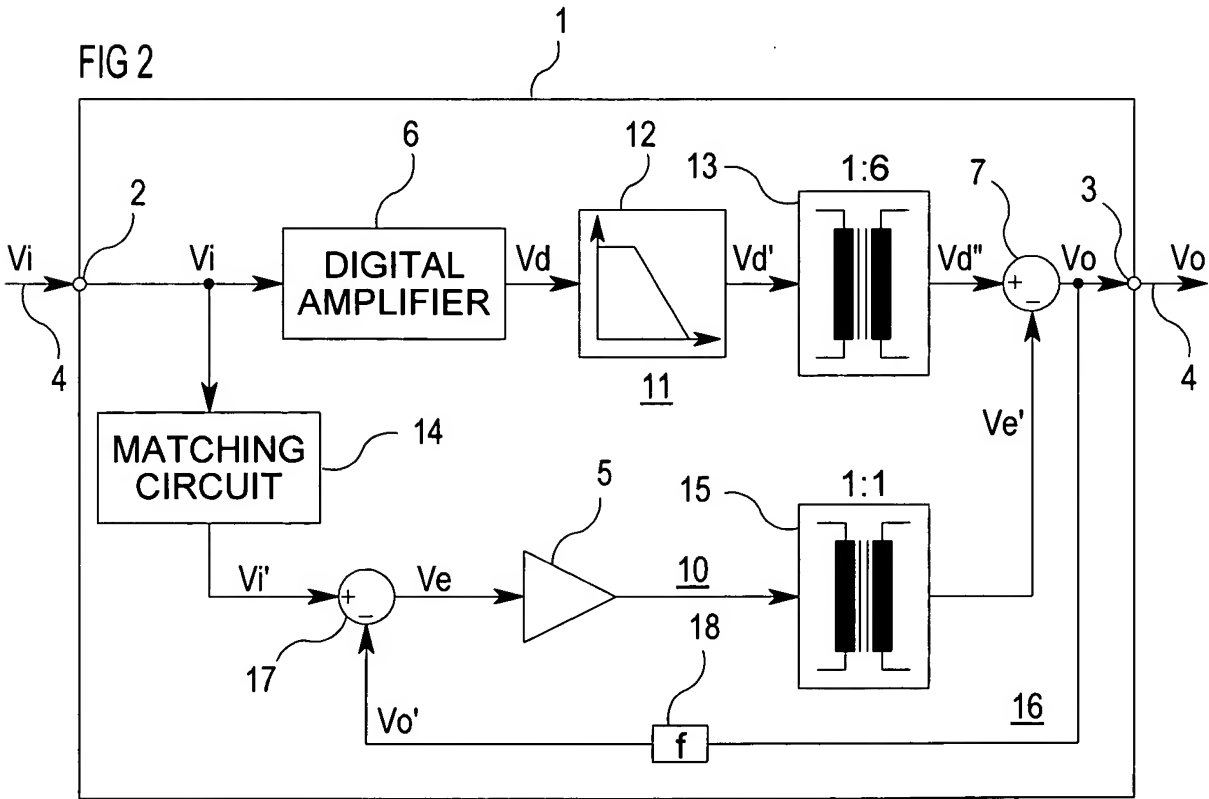
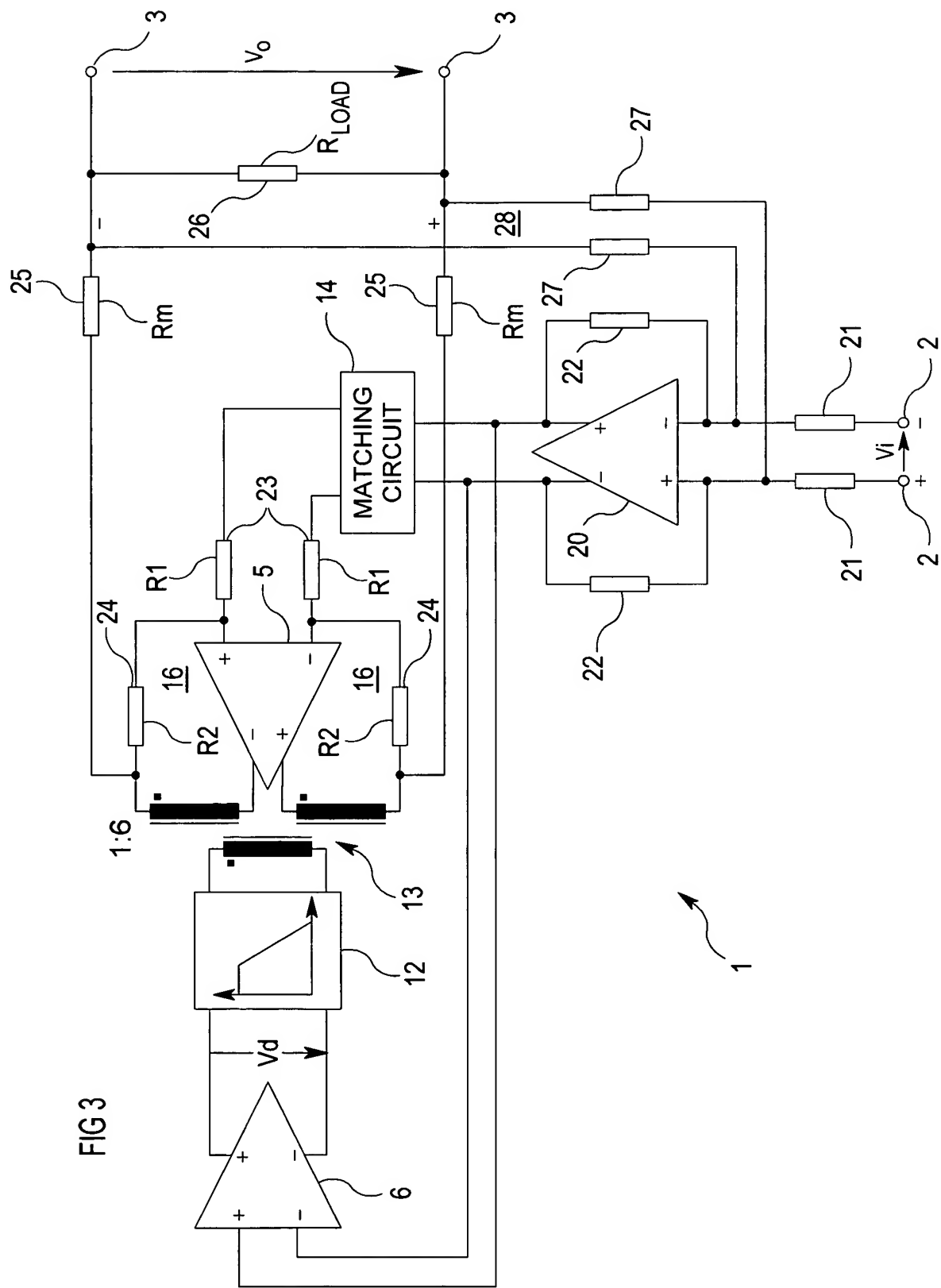


FIG 2





[illegible]

FIG 5

1

V_i

V_i

V_e

17

5

V_o'

40

RESISTANCE NETWORK

V_s

6

7

i_a

i_o

13

V_o

V_{line}

DIVIDER

18

41

V_f